

n-PRO-00 Reference Design

n-Blocks

# n-PRO-00

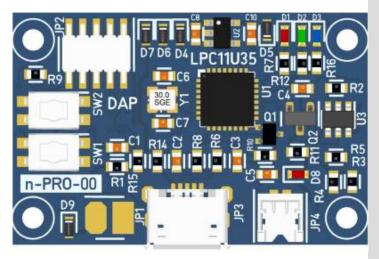
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#### n-PRO-00

n-PRO-00 is an easy to use ARM Cortex-M0 rapid prototyping board targeting relatively less complicated control applications and power efficient applications. The form factor of n-Blocks PRO makes it perfectly suitable for seamless integration.





n-PRO-00

License	GPL 2.0
Status	Tested
Buy at:	
Categories	
Hardware repo	Bitbucket
Firmware repo	

#### **Overview**

n-PRO-00 features LPC11U35 microcontroller from NXP. Since no cables are required, it is easy to use and can be used for rapid prototyping and proof of concept related applications. It contains a Cortex-M0 core that can operate at frequencies up to 50 MHz. The external 12 Mhz crystal provides standard serial bit rates and maximum execution speed. LPC11U35 has an on-chip ROM with In-System Programming capability which supports UART and USB flash programming. The flash API implements a simple interface to the on-board flash programming functionality and allows entry to ISP mode at any time. There is no need to write the drivers for basic interface of Human Interface Devices (HID) and Mass storage Class (MSC) devices in case of USB API development environment.



#### **MCU Features**

- Arm® Cortex-M0 processor, running at frequencies of up to 50 MHz
- Arm Cortex-M0 built-in Nested Vectored Interrupt Controller (NVIC)
- Up to 128 kB on-chip flash memory, 4 kB on-chip EEPROM data memory
- Up to 12 kB SRAM data memory, 16 kB boot ROM
- In-System Programming (ISP) and In-Application Programming (IAP)
- USB 2.0 full-speed device controller
- USARTs with fractional baud rate generation, internal FIFO, and RS-485/9 support
- 2 × SSP controllers with FIFO and multi-protocol capabilities
- I2C bus interface
- I/O Handler for hardware emulation of serial interfaces and DMA
- 54  $\times$  GPIOs with configurable pull-up/down resistors
- High-current source output driver (20 mA) on one pin
- High-current sink driver (20 mA) on true open-drain pins
- 10-bit ADC with input multiplexing among eight pins
- 4 × general purpose timers/counters
- Standard JTAG (Joint Test Action Group) test interface for BSDL
- Serial Wire Debug
- Integrated PMU (Power Management Unit)
- 4 reduced power modes: Sleep, Deep-sleep, Power-down, and Deep power-down
- Single 3.3 V power supply (2.4 V to 3.6 V)
- 4 × external interrupt inputs configurable as edge/level sensitive
- Non-maskable Interrupt (NMI) input
- Processor wake-up from Power-down mode via any interrupt
- Brownout detect with separate threshold for interrupt and forced reset
- Power-On Reset (POR)
- Crystal oscillator with an operating range of 1 MHz to 25 MHz
- 12 MHz internal RC oscillator, can be used as a system clock
- Unique device serial number for identification purposes

# n-PRO-00 Features

- Prototyping form-factor
- 5V USB or 4.5-5.5V supply
- Built-in USB drag 'n' drop FLASH programmer
- Reset and Bootloader enable push-buttons
- Mbed compatibility
- Cortex debug interface connector



- On-Chip Bootloader- In-System programming (ISP) and In-application programming (IAP)
- ROM-based USB drivers Flash updates via USB supported

#### **Board Pinout**

n-PRO-00 is a **HOST** board with two Hirose DF30-series 60-pin low profile connectors at bottom side, following the n-Blocks PRO form factor.





LPCTIU35 PIN	FUNCTION	GPIO	MAIN FUNCTION	CON PIN NO	NBUS-A		CON PIN NO	MAIN FUNCTION	GPIO	FUNCTION	LPCTIU35 PIN
			1-WIRE	A60			A1	1.8V			
			GND	A59		1	A2	GND			
			+2.8V	A58			A3	+2.8V			
			CAN2-RD	A57	TEI I		A4	RXD1	P2.0	RXD	31
			CAN2-TD	A56			A5	TXD1	P2.1	TXD	32
25	ADC4	P0.3	RXD0	A55			A6	SDA1	P0.10	SDA1	11
			T XD0	A54			A7	SCL1	P0.11	SCL1	10
27	P0.17		PWM1	A53			A8	MIS00			
26	P0.16		PWM2	A52			A9	MOSIO	P0.18	MOSIO	18
23	ADC2		ADC3	A51			A10	SCK0	P0.15	SCK	19
24	ADC3		ADC2	A50			A11	SS0			
1	P1.19		CLKOUT	A49			A12	RESET-CPU	RESET-LPC11		
			VBAT	A48			A13	USB-CON			
			MOSFET	A47			A14	QENCB			
30	PI00-17		CS1	A46			A15	ADCO	P0.23	ADC0	20
28	PI00-15/SCK1		SCK1	A45			A16	ADC1	P0.24	ADC1	22
			M0SI1	A44			A17	EINT3			
			MIS01	A43	<b>H H H H</b>		A18	EINT2			
			SCL2	A42	<b>.</b>		A19	USB-DP	P0.29	LPC-11-D+	14
			SDA2	A41			A20	USB-DN	P0.30	LPC11-D-	13
			TXD4	A40			A21	CAN1-TD			
			RXD4	A39			A22	CAN1-RD			
				A38			A23	SLEEP			
				A37	- I - I -		A24	SENSOR1			
			-	A36	- I I		A25	SENSOR2			
			-	A35	- I I I I I I I I I I I I I I I I I I I		A26	SENSOR3			
			-	A34			A27	SENSOR4			
			+5V	A33			A28	+5V			
			GND	A32			A29	GND			
			3.3V	A31	<b>I</b> I I		A30	3.3V			
					Ľ						



LPCTIU35 PIN	FUNCTION	0	MAIN FUNCTION		CON PIN NO			N	NBL	JS-	-B			CON PIN NO		MAIN FUNCTION	0	FUNCTION	LPC11U35 PIN
LPC	FUN	GPIO	MAI		B									CON		MAI	GPIO	FUN	LPC
			1WIRE	B	60	-		Ξ			Ξ		H	B1	-	AREFO			
			GND	В	59	-	-	T			I	1		B2	-	GND			
			V-IO	B	58			1.1			I	÷		B3	-	AREF1			
			ISP	В	57 -	-	ł	I			I	÷		B4	-				
			LED1	B	56	-	-	нинининининининининин			I	÷		B5					
			LED2	В	55	-		T			I	÷		B6	-	SDA3			
			LED3	B	54	-	ł	I			I	÷		B7	-	SCL3			
			LED4	B	53	-	-	Ξ			I	÷		B8	-				
			GPIO	B	52		-	1 1			II	÷		B9	-				
			GPIO	B	51 -		ł	I			I	÷		B10	-				
			GPIO	B	50 -	-	ł	1.1			Ξ.	÷		B11	-				
16		SWDIO-TGT	SWDIO	B	49		ł	I			Ξ.	÷		B12	F	RESET-CPU	RESET-TGT		8
			SLEEP	B	48		-	1			Ξ.	÷		B13	-				
17		SWCLK-TGT	SWDCLK	B	47		-	I			I	÷		B14	-				
			J-TDO	B	46	-	ł				Ξ.	÷		B15	-				
			J-TDI	B	45		-	I.I			I	÷		B16					
			GPIO	B	44		-	1 1			II	÷		B17					
			GPIO	B	43			I.I			I	÷		B18					
			SCL4	B	42		-	1 1			Ι.	÷		B19	-				
			SDA4	B	41 -		-	T			Ξ.	÷		B20					
			GPIO	B	40		-	1 1			II	÷		B21					
			GPIO	B	39		-	I I			Ξ.	÷		B22					
				B	38		-	I			II	÷		B23					
				B	37	-	-	I			Ι.	÷		B24	F	RESET-OUT			
				B	36	-	ł	11			II	÷		B25		SENSOR2			
				B	35						I	•		B26		SENSOR3			
			2 2	B	34		-	H			I	÷		B27	-	SENSOR4			
			+5V	B	33 -		ł	I			Ξ.	÷		B28	-	+5V			
			GND	B	32 -	-	-				II	÷		B29	-	GND			
			3.3V	B	31 -		-	H				-	$\square$	B30		3.3V			
								l			J				ŝ				

# **Getting started**

# Blinky with mbed compiler

- With a USB cable connected, hold down the BL button and press RESET
- You should see the green USB light come on, at which point you can release the BL button
- After a few seconds a mass storage device named CRP\_DISABLED will show up on your computer
- On it will be a single 64KB file, firmware.bin, that represents the contents of the flash



Computer + CRP DISA			• 44 Searc	A CRP DISAB		-	
Organize * Share with * Burn	Nev	v folder			)E •	0	
J Music	*	Name	Date modified	Туре		Size	
Pictures		🖻 firmware.bin	2/6/2009 10:10 AM	BIN File		1	28 10
Computer	٠						

- Select target on mbed online compiler
- Create a blinky program like below

```
#include "mbed.h"
DigitalOut led1(P0_20);
// main() runs in its own thread in the OS
int main() {
    while (true) {
        led1 = !led1;
        wait(0.05);
    }
}
```

- Delete the existing firmware.bin file, and replace it with the .bin file you downloaded from the mbed compiler.
- Press RESET again, and the board will now be running your code.

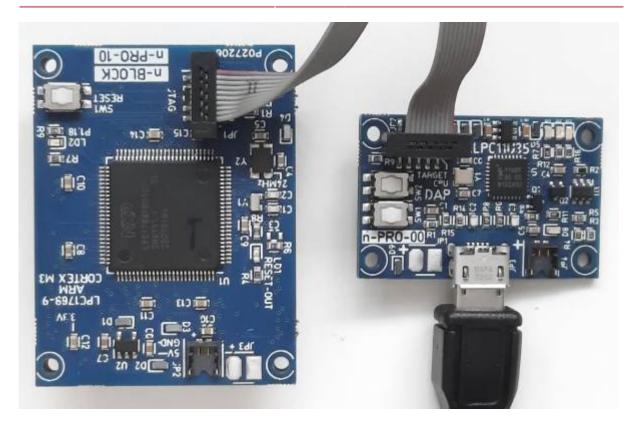
# **USB Serial Port**

• To use the debug serial port of the LPC11U35 to emulate a serial port over USB see the example here.

# Using n-PRO-00 as drag n drop programming tool

The 10-pin JTAG connector available on board, is to program a target ARM-Cortex board (Not the n-PRO-00 LPC11U35 Microcontroller).





To be able to use the n-PRO-00 as programming tool we first need to program it with one of the binaries below:

Once the CMSIS-DAP firmware is programmed to n-PRO-00 LPC11U35 Flash memory, it appears as a USB disk.

Now we are ready to drag n drop the binary for the target board to the USB disk.

# LPC1768/LPC812

- LPC11U35-Firmware for communicating with LPC1768 (Does not create virtual serial port)
- SWDAP-LPC11U35 NXP LPC1768 PREBUILD IMAGE (Creates a virtual serial port)

# LPC1114FN28

• Firmware LPC1114FN28 (Firmware to use n-DAP with the non-SMD LPC1114FN28 DIP-28)



#### nRF51822

- Seeed studio wiki Arch BLE
- Seeed studio wiki File:Lpc11u35 nrf51822 if mbed.bin.zip
- Bootloader for BLE mbug

#### NXP K64F

- https://os.mbed.com/platforms/SWDAP-LPC11U35/NXP K64F
- https://os.mbed.com/media/uploads/chris/lpc11u35\_swdap\_k64f\_if\_crc.bin bin

# **CMSIS-DAP Interface Firmware**

• CMSIS-DAP Interface Firmware

#### References

- LPC11U35 Datasheet
- https://os.mbed.com/platforms/EA-LPC11U35/
- https://www.embeddedartists.com/products/lpc11u35-quickstart/

# **Related articles in this Wiki**

- n-pro-00
- RF, CPU, nblock, BLE, nsensorRF

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Address: Cork Institute of Technology Campus, Bishopstown, Cork

Phone: (021) 433 5560

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